

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	1536	257/786.ccls.	US-PGPUB; USPAT; USOCR	2005/09/11 17:38	
2	BRS	L2	1926	257/773.ccls.	US-PGPUB; USPAT; USOCR	2005/09/11 17:32	
3	BRS	L3	288	438/601.ccls.	US-PGPUB; USPAT; USOCR	2005/09/11 17:33	
4	BRS	L4	3259	Chip adj scale adj package	US-PGPUB; USPAT; USOCR	2005/09/11 17:38	
5	BRS	L5	15	L1 and (terminal or electrode) and insulating and (input or output or I/O) and fuse	US-PGPUB; USPAT; USOCR	2005/09/11 17:37	
6	BRS	L6	11	L2 and (terminal or electrode) and insulating and (input or output or I/O) and fuse	US-PGPUB; USPAT; USOCR	2005/09/11 17:37	
7	BRS	L7	18	L3 and (terminal or electrode) and insulating and (input or output or I/O) and fuse	US-PGPUB; USPAT; USOCR	2005/09/11 17:37	
8	BRS	L8	34	L4 and (terminal or electrode) and insulating and (input or output or I/O) and fuse	US-PGPUB; USPAT; USOCR	2005/09/11 17:37	
9	BRS	L9	10	L5 and (bump or ball)	US-PGPUB; USPAT; USOCR	2005/09/11 17:38	
10	BRS	L10	3	L6 and (bump or ball)	US-PGPUB; USPAT; USOCR	2005/09/11 17:38	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
11	BRS	L11	3	L7 and (bump or ball)	US-PGPUB; USPAT; USOCR	2005/09/11 17:38	
12	BRS	L12	34	L8 and (bump or ball)	US-PGPUB; USPAT; USOCR	2005/09/11 17:38	
13	BRS	L13	356	257/786.ccls.	EPO; JPO; DERWENT; IBM_TDB	2005/09/11 17:38	
14	BRS	L14	449	257/773.ccls.	EPO; JPO; DERWENT; IBM_TDB	2005/09/11 17:38	
15	BRS	L15	1709	Chip adj scale adj package	EPO; JPO; DERWENT; IBM_TDB	2005/09/11 17:38	